Medium Power Transistor (-50V, -1A)

2SA1900

Features

- 1) Low saturation voltage, typically $V_{CE(sat)}$ =–0.15V at Ic / I_B=–500mA / –50mA.
- 2) Pc = 2W (on 40 \times 40 \times 0.7 mm ceramic board.)
- 3) Complements the 2SC5053.

Packaging specifications and hre

Туре	2SA1900
Package	MPT3
hfe	Q
Marking	AL*
Code	T100
Basic ordering unit (pieces)	1000

* Denotes hre

●Absolute maximum ratings (Ta=25℃)

Parameter	Symbol	Limits	Unit		
Collector-base voltage	Vсво	-60	V		
Collector-emitter voltage	VCEO	-50	v		
Emitter-base voltage	VEBO	-5	v		
Collector current	lc	-1	A		
	IC	-2	A (Pulse)	*1	
Collector power dissipation	Po	0.5	w		
	FC	2	W	*2	
Junction temperature	Тj	150	°C		
Storage temperature	Tstg	-55~+150	°C		

*1 Single pulse Pw=10ms, Duty=1/2

*2 When mounted on a 40 \times 40 \times 0.7 mm ceramic board.

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВVсво	-60	-	-	V	$lc = -50 \mu A$
Collector-emitter breakdown voltage	BVCEO	-50	-	-	V	Ic=-1mA
Emitter-base breakdown voltage	ВVево	-5	-	_	V	IE=-50 μ A
Collector cutoff current	Ісво	_	-	-0.1	μA	V _{CB} =-40V
Emitter cutoff current	Іево	-	-	-0.5	μA	VEB=-4V
Collector-emitter saturation voltage	VCE(sat)	_	-	-0.4	V	lc/lB=-500mA/-50mA
DC current transfer ratio	hre	120	-	270	—	Vce/lc=-3V/-0.5A
Transition frequency	fτ	_	150	—	MHz	Vce=-5V, le=50mA, f=100MHz
Output capacitance	Cob	—	20	—	pF	VcB=-10V, IE=0A, f=1MHz

(96-115-B352)

Medium Power Transistor (50V, 1A)

2SC5053

Features

- 1) Low saturation voltage, typically $V_{CE(sat)} = 0.12V$ at Ic / IB=500mA / 50mA.
- 2) Pc = 2 W (on 40 \times 40 \times 0.7 mm ceramic board)
- 3) Complements the 2SA1900

Packaging specifications and hre

Туре	2SC5053
Package	MPT3
hfe	QR
Marking	CG*
Code	T100
Basic ordering nuit (pieces)	1000

* Denotes hre

●Absolute maximum ratings (Ta=25℃)

Parameter	Symbol	Limits	Unit		
Collector-base voltage	Vсво	60	V		
Collector-emitter voltage	VCEO	50	V		
Emitter-base voltage	VEBO	5	v		
Collector current	lc	1	A (DC)		
	IC	2	A (Pulse)	*1	
Collector power dissipation	Pc	0.5	w		
	PC	2	W	*2	
Junction temperature	Tj	150	Ĵ		
Storage temperature	Tstg	$-55 \sim +150$	ů,		

*1 Single pulse Pw=20ms, Duty=1/2

*2 When mounted on a 40 \times 40 \times 0.7 mm ceramic board.

●Electrical characteristics (Ta=25℃)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	60	—	—	V	Ic=50 μ A
Collector-emitter breakdown voltage	BVCEO	50	_	_	V	Ic=1mA
Emitter-base breakdown voltage	ВVевф	5	-	_	V	IE=50 μ A
Collector cutoff current	Ісво	—	-	0.1	μA	Vcb=40V
Emitter cutoff current	Ієво	—	-	0.1	μA	VEB=4V
Collector-emitter saturation voltage	VCE(sat)	—	-	0.4	V	Ic/IB=500mA/50mA
DC current transfer ratio	hfe	120	-	390	-	Vce/lc=3V/0.5A
Transition frequency	fr	_	150	-	MHz	Vce=5V, le=-50mA, f=100MHz
Output capacitance	Cob	_	15	—	pF	V _{CB} =10V, I _E =0A, f=1MHz

(96-196-D352)



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